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APPLICANT Volkan Kursun et al

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Not yet assigned**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOC. NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
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**FOREIGN PATENT DOCUMENTS**

		DOC. NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AO				<input type="checkbox"/>	<input type="checkbox"/>
	AP				<input type="checkbox"/>	<input type="checkbox"/>
	AQ				<input type="checkbox"/>	<input type="checkbox"/>
	AR				<input type="checkbox"/>	<input type="checkbox"/>
	AS				<input type="checkbox"/>	<input type="checkbox"/>
	AT				<input type="checkbox"/>	<input type="checkbox"/>
	AU				<input type="checkbox"/>	<input type="checkbox"/>
	AV				<input type="checkbox"/>	<input type="checkbox"/>

**OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)**

AT	AW	V. Kursun and E. G. Friedman, "Domino Logic with Variable Threshold Voltage Keeper," IEEE Transactions on Very Large Scale Integration (VLSI) Systems (In press).
	AX	V. Kursun and E. G. Friedman, "Variable Threshold Voltage Keeper for Contention Reduction in Dynamic Circuits," Proceedings of the IEEE International ASIC/SOC Conference, pp. 314-318, September 2002
D	AY	V. Kursun and E. G. Friedman, "Domino Logic with Dynamic Body Biased Keeper," Proceedings of the European Solid-State Circuits Conference, pp. 675-678, September 2002.
	AZ	

Examiner

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